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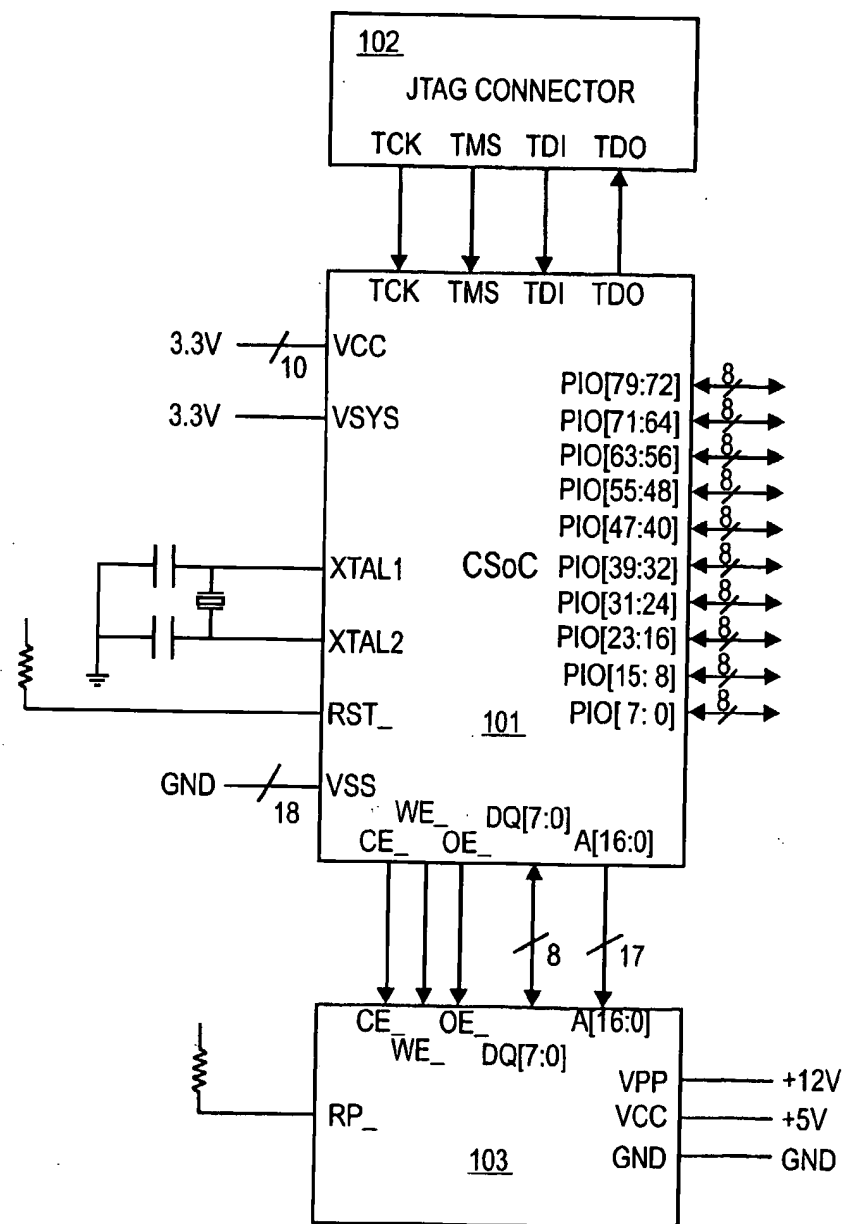


FIG. 1

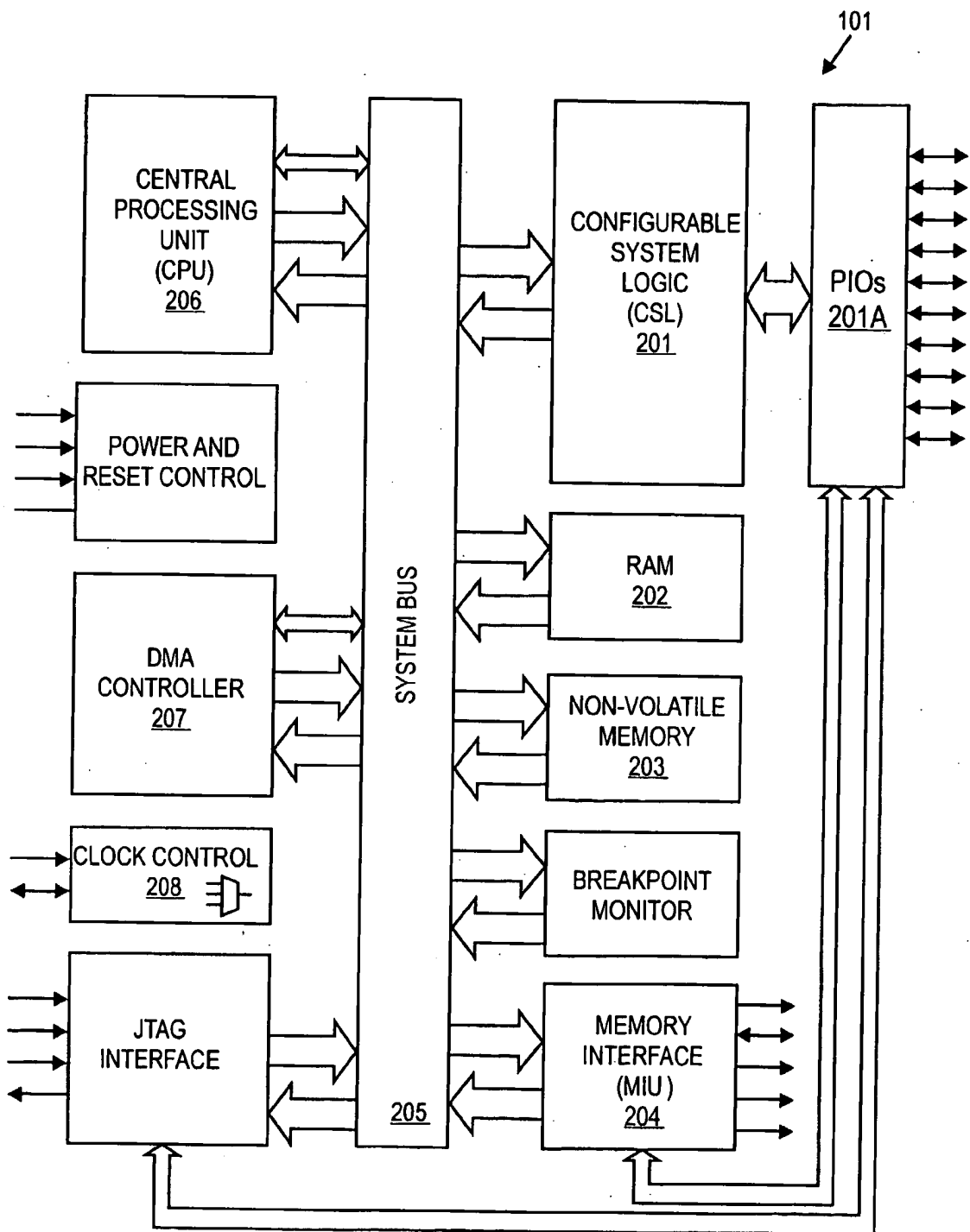


FIG. 2

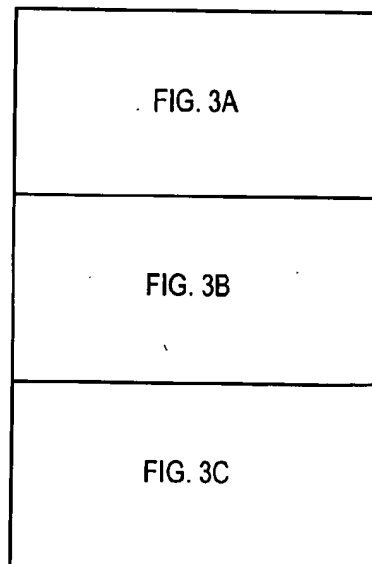


FIG. 3

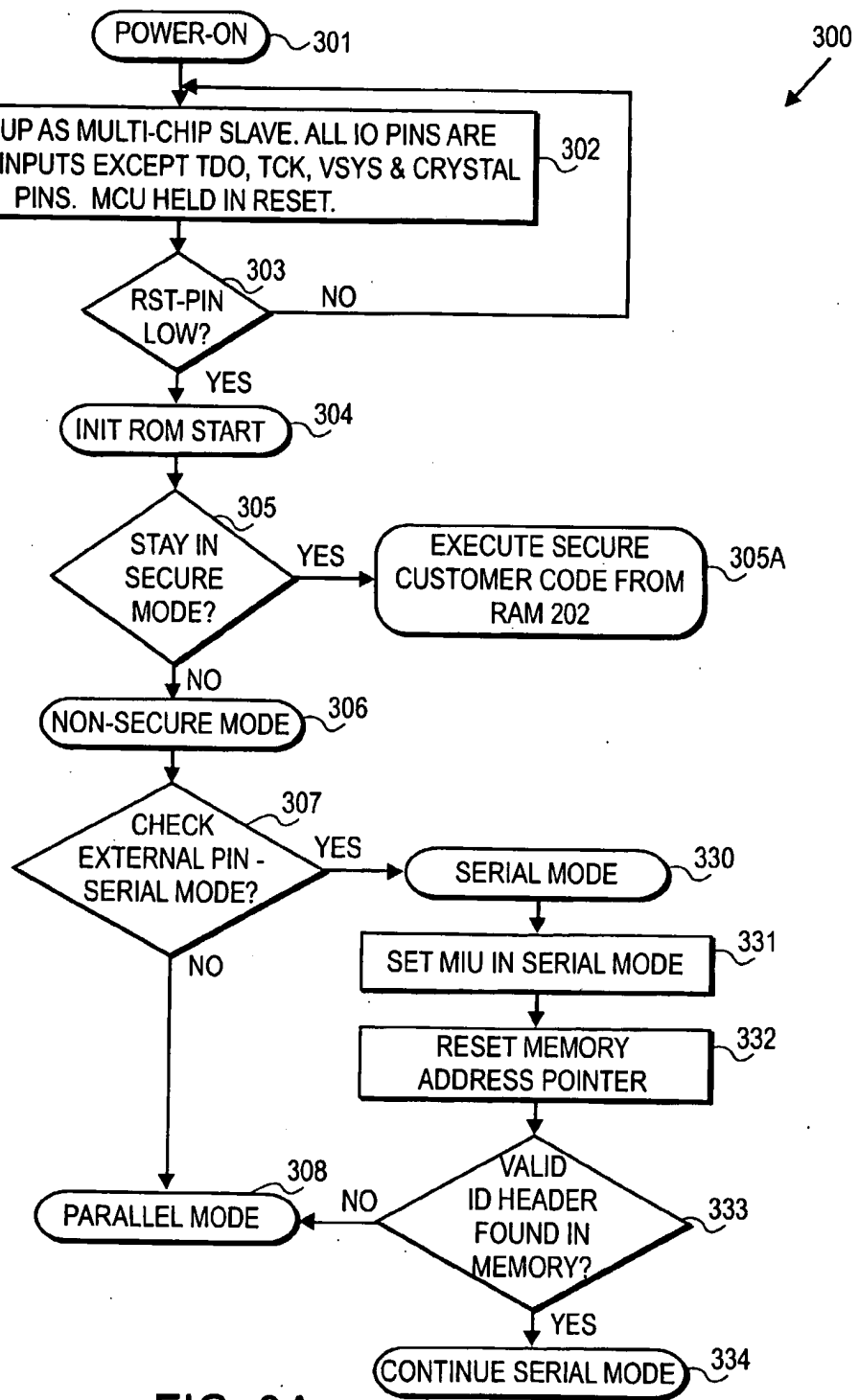


FIG. 3A

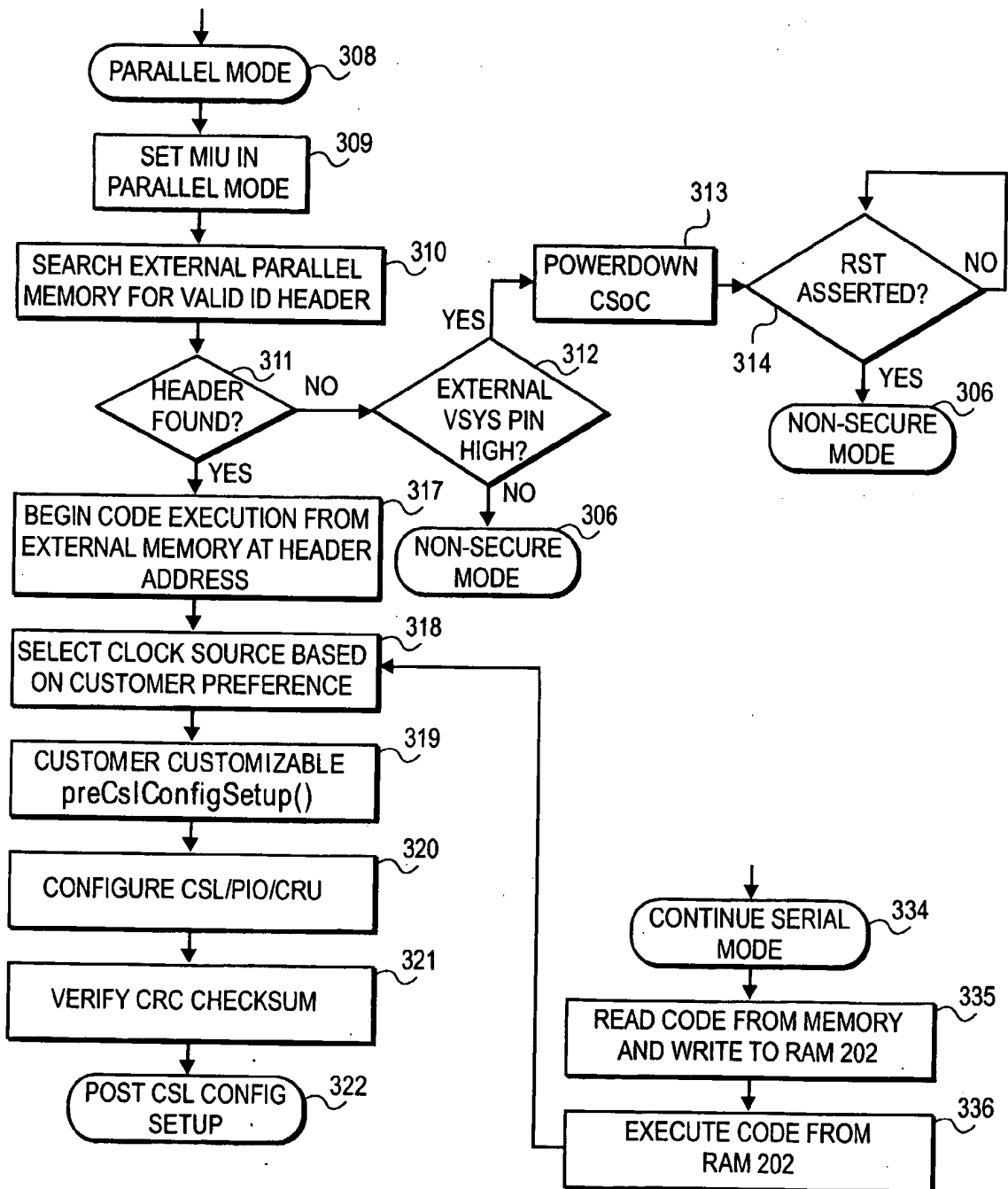


FIG. 3B

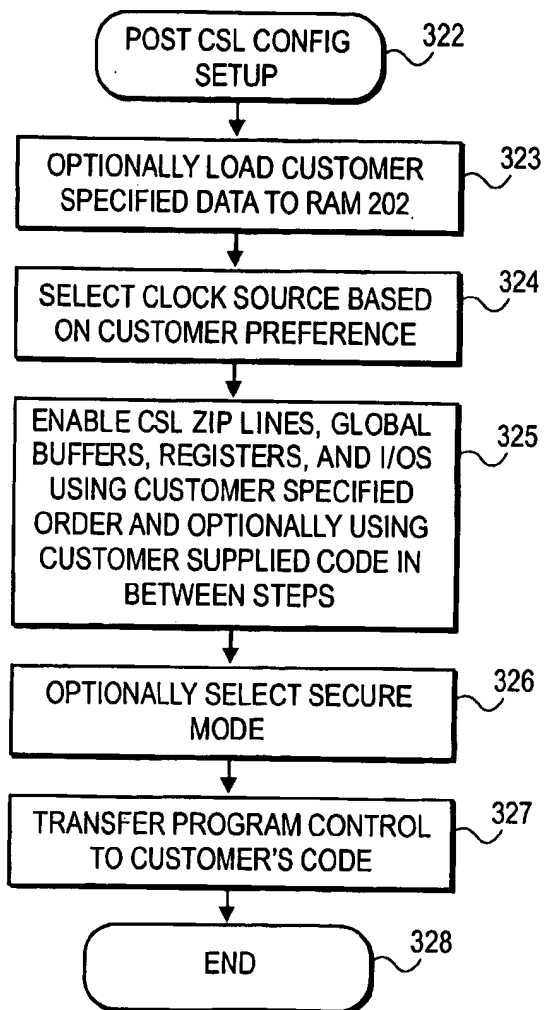


FIG. 3C

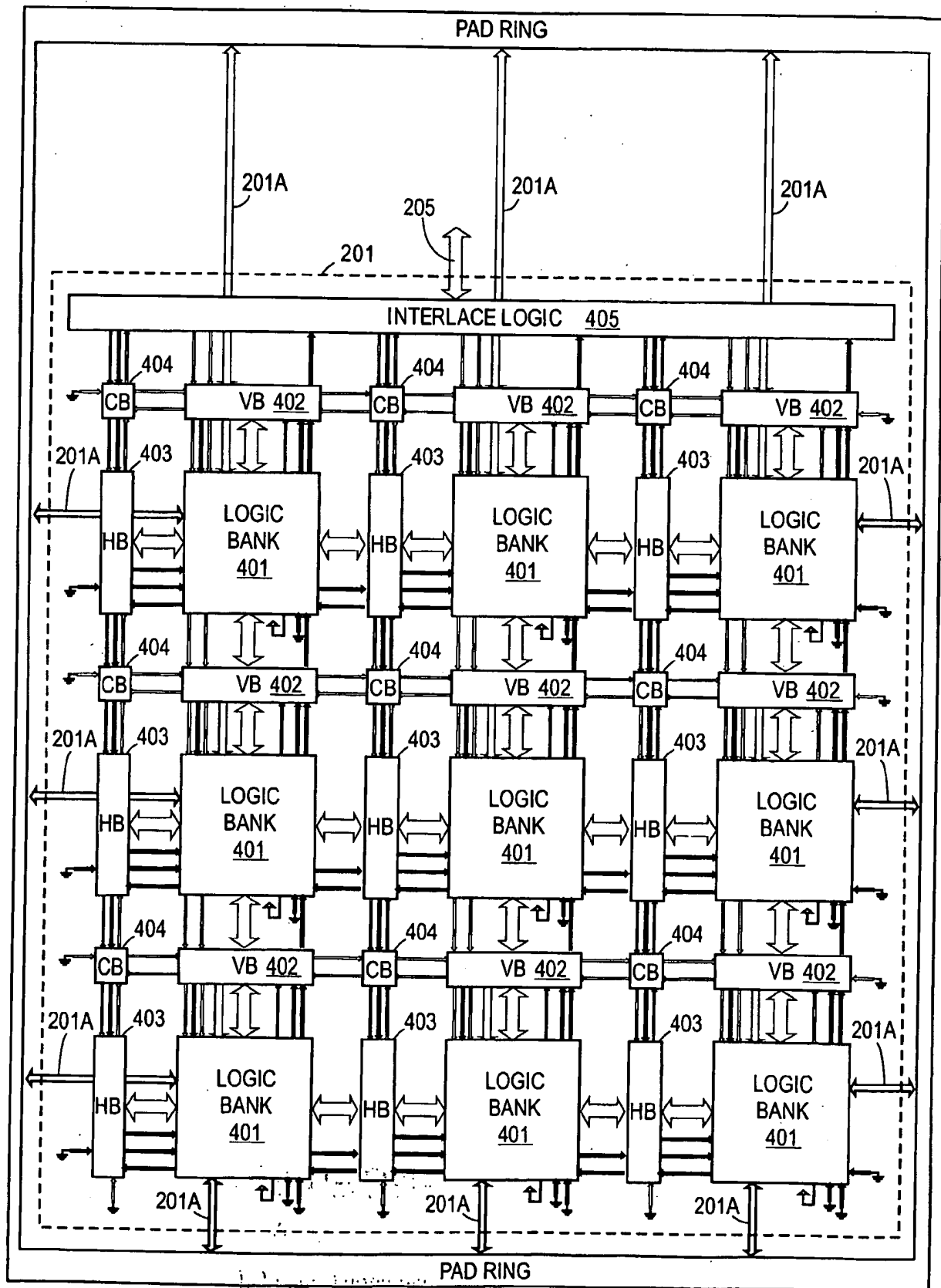


FIG. 4

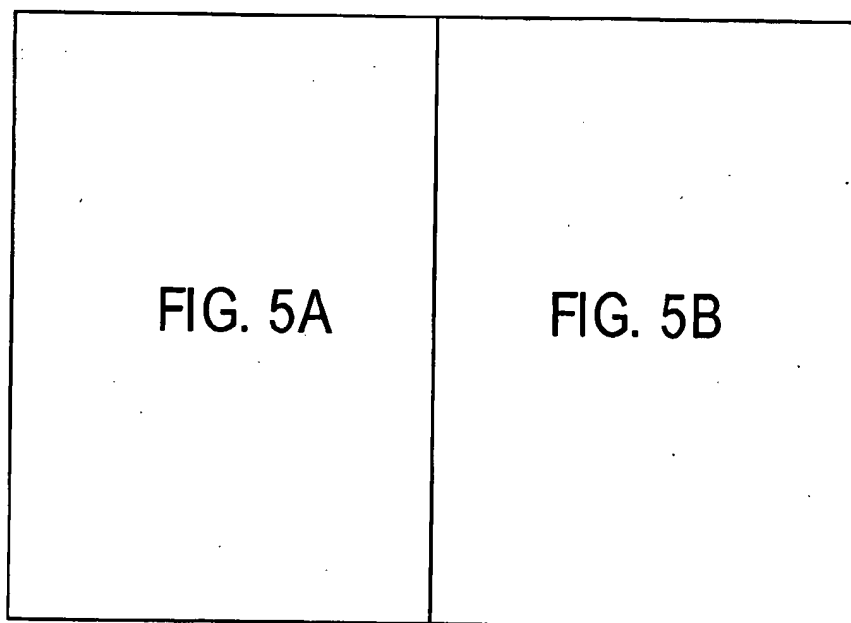


FIG. 5

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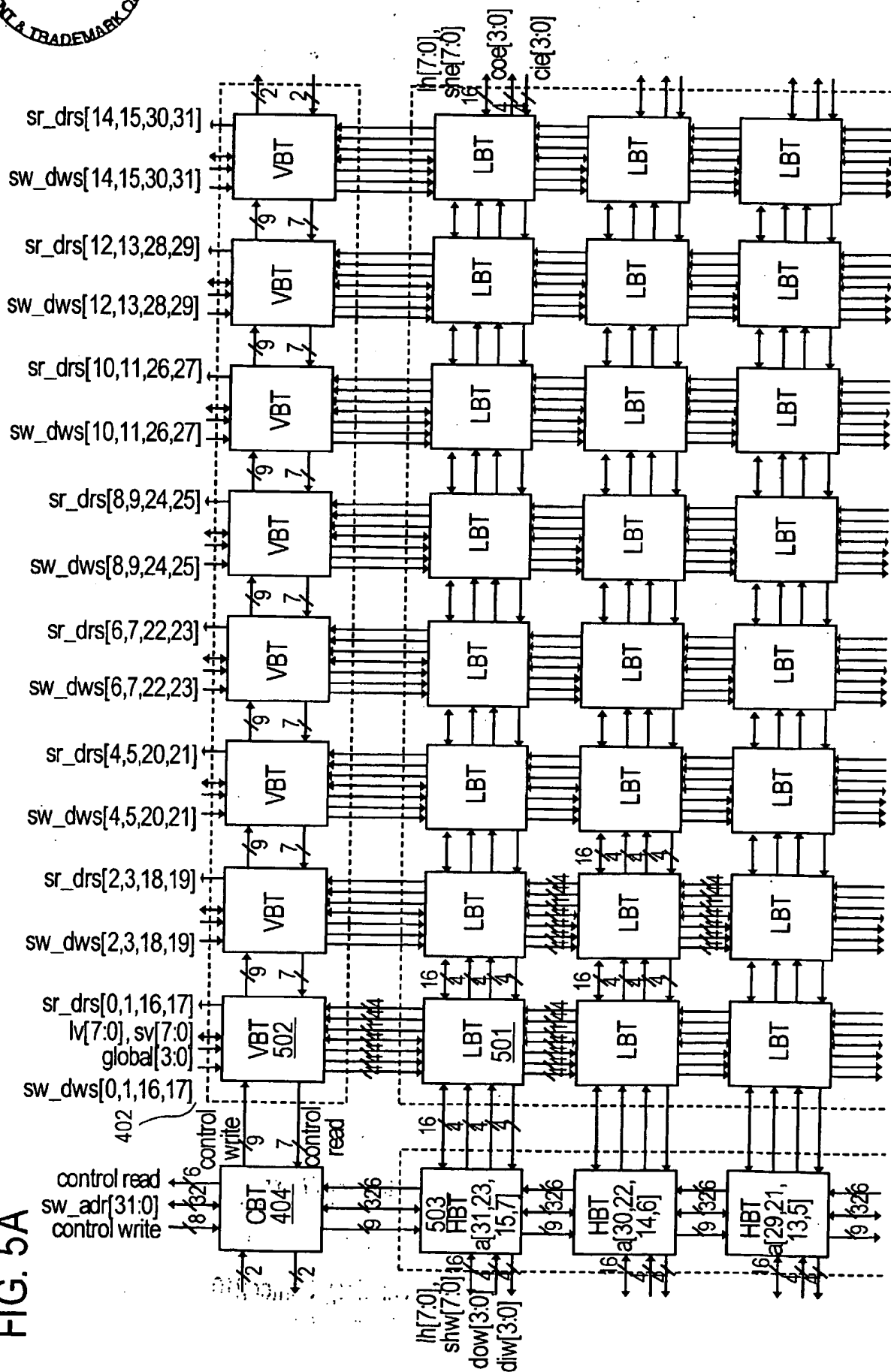
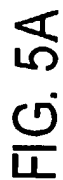


FIG. 5B

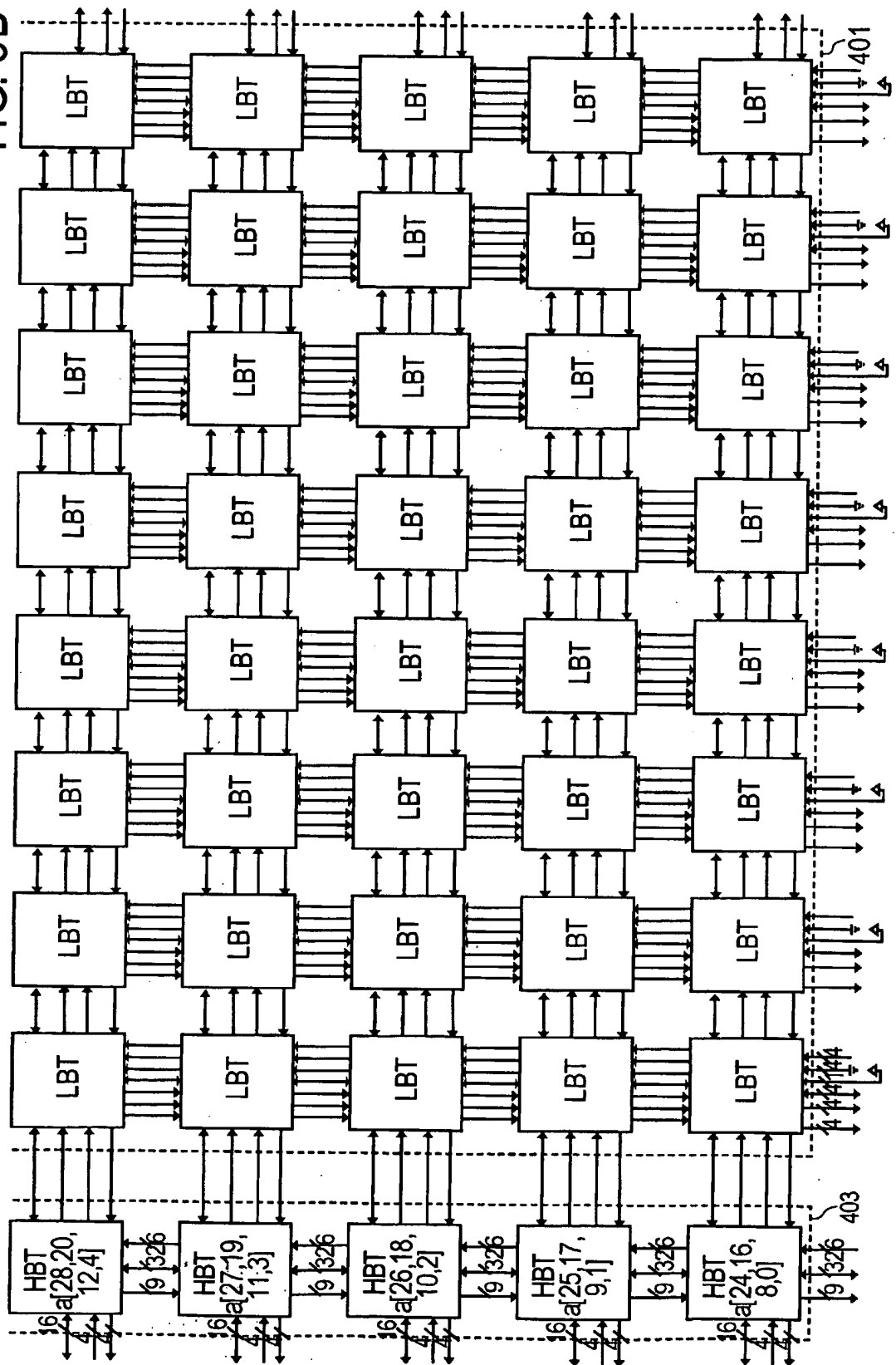




FIG. 6C

FIG. 6A

FIG. 6B

FIG. 6

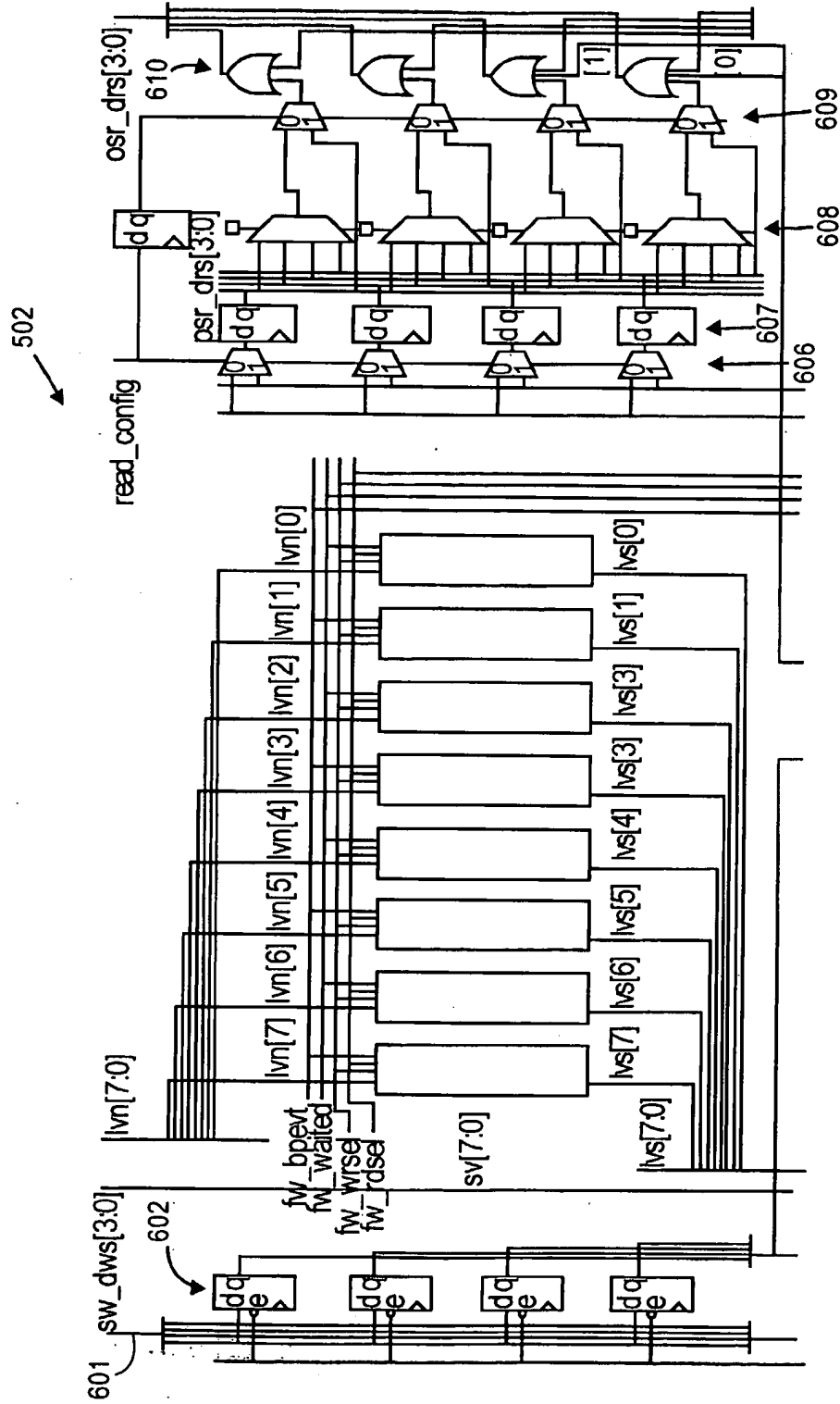


FIG. 6A

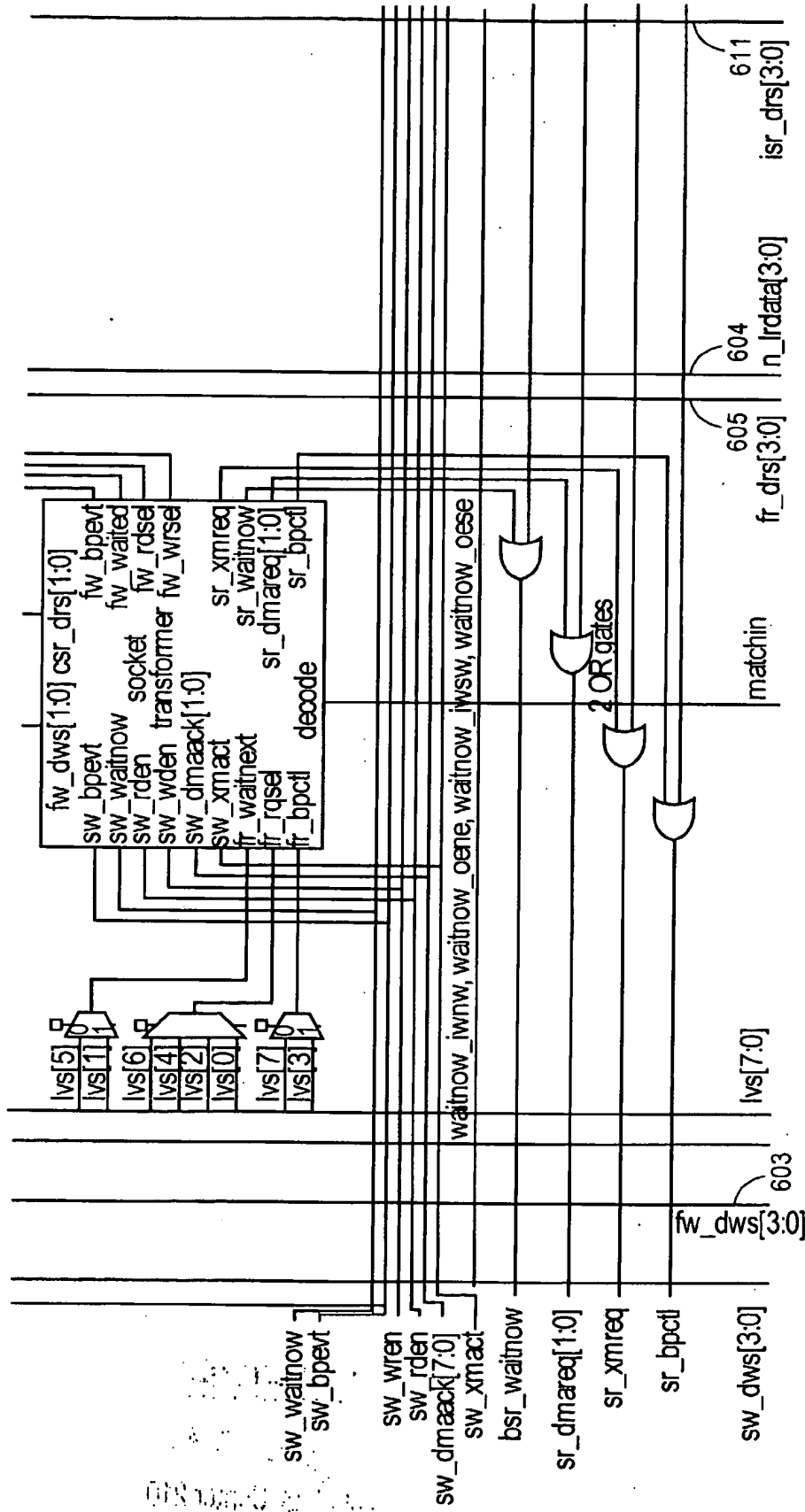


FIG. 6B

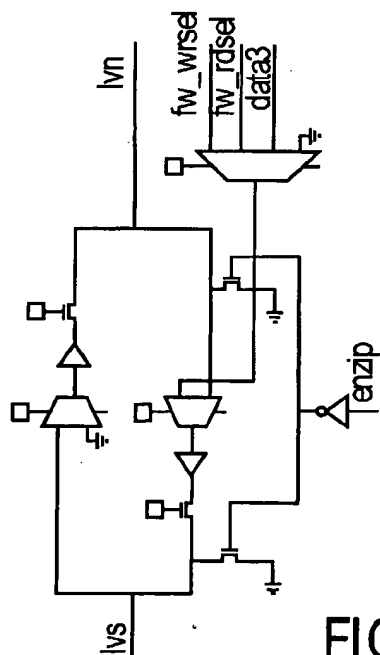


FIG. 6C

vertical connector of vertical breaker tile

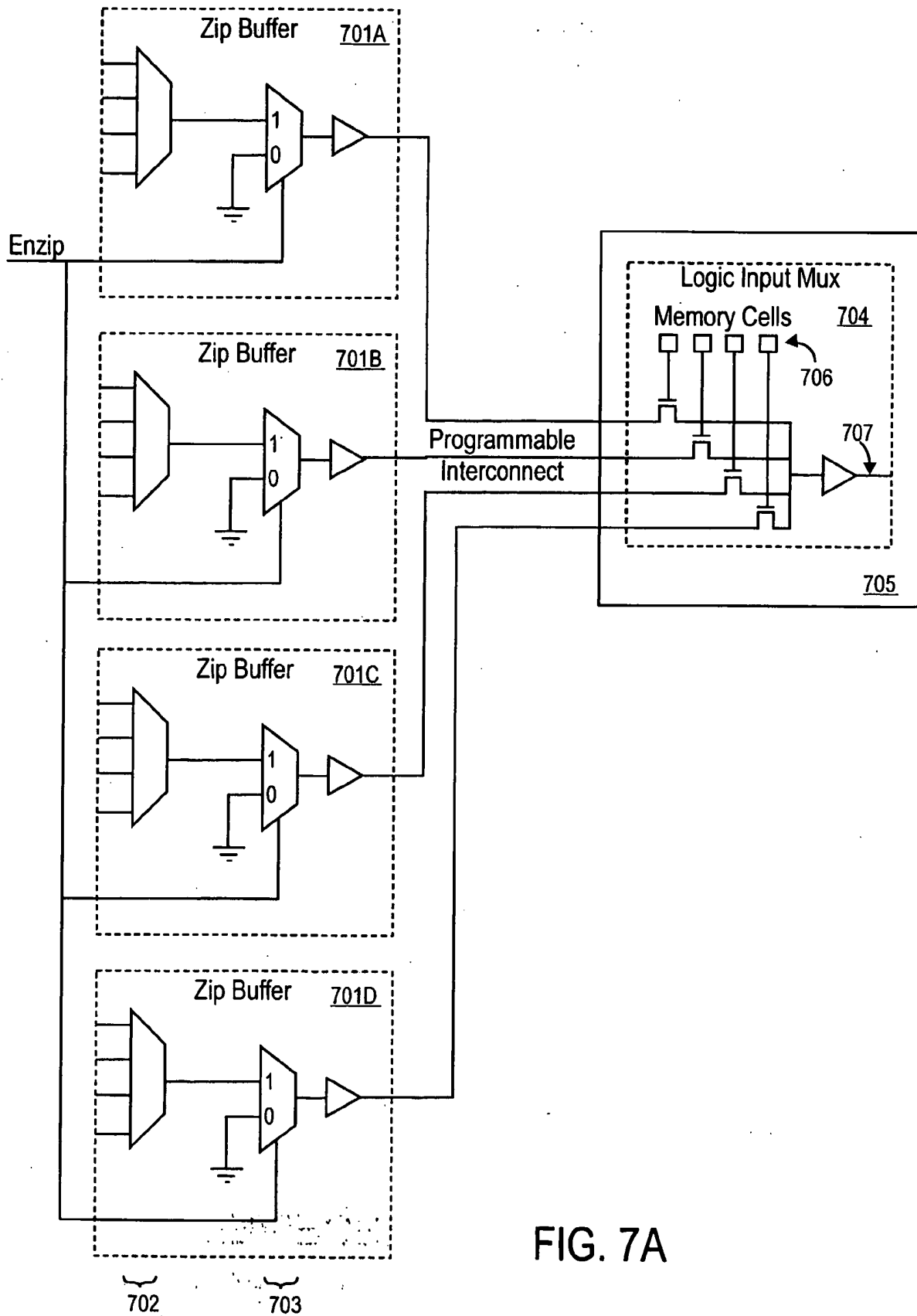
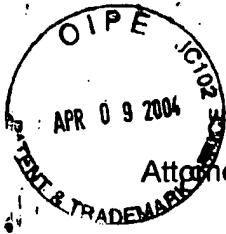


FIG. 7A





Attorney Docket No.: 3242P027

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Brian Fox, et al.

Application No.: 09/419,386

Filed: October 15, 1999

For: CONFIGURATION IN A
CONFIGURABLE SYSTEM
ON A CHIP

Examiner: Bragdon, Reginald Glenwood

Art Unit: 2188

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SUBMISSION OF FORMAL DRAWINGS

Sir:

Transmitted herewith are sixteen (16) sheets of formal drawings for the
above-identified application.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: Apr. 19, 2004

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